Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**PAD FUNCTION:**

1. **CATHODE**
2. **ANODE**
3. **REFERENCE**

**.030”**

**1 2 3**

**.024”**

**NOTE: CONNECT CHIP BACK TO ANODE**

**Top Material: Al**

**Backside Material: Bare Si**

**Bond Pad Size: .004 x .004”**

**Backside Potential: N/A**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .024” X .030” DATE: 4/27/23**

**MFG: SILICON SUPPLIES THICKNESS .011” P/N: TL431**

**DG 10.1.2**

#### Rev B, 7/1